

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Sokolov et al.

Attorney Docket No.: SUN1P811/P5512

Application No.: 09/819,120

Examiner: SHAH, NILESH R.

Filed: March 27, 2001

Group: 2195


Title: REDUCED INSTRUCTION SET FOR
JAVA VIRTUAL MACHINES

Confirmation No.: 4323

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on May 27, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: _____


Secretary

**INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)**

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of Foreign patents and Other Documents are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is

believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. SUN1P811).

Respectfully submitted,

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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. SUN1P811/P5512	Application No.: 09/819,120
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1	2002/0188726A1	12/12/02	Schick et al.			06/08/01
	A2	2002/0170041A1	11/14/02	Shann			12/20/01
	A3	4,910,731	03/20/90	Sakurai et al.			07/13/88
	A4	5,418,964	05/23/95	Conner et al.			08/31/94
	A5	5,815,718	09/29/98	Tock			05/30/96
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	A31	6,584,612 B1	06/24/03	Mueller et al.			07/15/99
Examiner				Date Considered			

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
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	A36	6,738,977 B1	05/18/04	Berry et al.			05/31/00
	A37	6,851,108 B1	02/01/05	Syme et al.			11/16/00
	A38	6,851,111 B2	02/01/05	McGuire et al.			12/15/00

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B1	EP0810522 A2	12/03/97	EPO				
	B2	EP0943989 A2	09/22/99	EPO				
	B3	WO99/31576	06/24/99	PCT				

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C1	Patrice Pominville, "Annotating Java Bytecode," Project Report, McGill University, Online, April 2000, pages 1-7. XP-002254149
	C2	Yourst, "Inside Java Class Files," Dr. Dobb's Journal, Jan. 1998, XP-002254150
	C3	Hummel et al., "Annotating the Java bytecodes in support of optimization," Concurrency: Practice and Experience, John Wiley and Sons, Vol. 9(11), November 1997, pages 1003-1016. XP-001131083
	C4	Lindholm et al., "The Java Virtual Machine Specification," Second Edition, April 1999, pages 117-132. XP-002254151
	C5	Dahm, Markus, "Byte Code Engineering", 1999, Java Information Tag, Proceedings of JIT' 99: Java-Informations'Tage, Dusseldorf, Germany, Vol. 20-21, pp. 267-277, XP-002262007.
Examiner		Date Considered

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Other Documents

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	C6	Bill Venners, "How the Java virtual machine handles method invocation and return" Java World, June 1997, pages 1-10
	C7	Clausen et al., "Java Bytecode Compression for Low-End Embedded Systems," 5/2000, ACM
	C8	Goldberg, "A specification of java loading and bytecode verification," ACM, pp 49-58, 1998
	C9	Chang et al. "Type based verification of assembly language for compiler debugging," ACM TLDI, pp 91-102, Jan. 2005
	C10	Qian et al., "A formal specification of Java class loading," ACM OOPSLA, pp 325-336, 2000
	C11	Krintz et al., "Reducing transfer delay using java class file splitting and prefetching," ACM OOPSLA, pp276-291 11/1999
	C12	Chilimbi et al., "Cache-Conscious Structure Definition", P. 13-24, 1999 ACM
	C13	Nicol et al., "Empirical Study of parallel Trace-Driven LRU Cache Simulators", p. 166-169, 1995 IEEE
	C14	Office Action of August 2, 2004 by Examiner Ali for Patent Application No. 09/820,097, entitled " <i>Enhanced Virtual Machine Instructions</i> "
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.